



FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.: 57983.000061	SERIAL NO.: 10/076,415		
LIST OF MATERIALS CITED BY APPLICANT (Use several sheets if necessary) Sheet 1 of 1				INVENTOR'S NAME: James AWEYA et al.	EXAMINER: Unassigned		
				FILING DATE: February 19, 2002	GROUP ART UNIT: 2181		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)							
SKS	9.	WILLIAM C. LINDSEY, Proceedings of the IEEE, Vol. 69, No. 4, April 1981, "A Survey of Digital Phase-Locked Loops", 22 pages					
SKS	10.	RICHARD C. LAU, IEEE Transactions On Communications, Vol. 43, No. 2/3/4, February/March/April 1995, "Synchronous Techniques for Timing Recovery in BISDN", pp. 1810-1818					

EXAMINER <i>Suresh K Suryawanshi</i>	DATE CONSIDERED <i>12/20/01</i>
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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